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PDF Cadence

Spectre User

Guide

# Spectre User Guide

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Cadence IC6.16/6.17

Virtuoso Tutorial -1

Part 2 (Simulation,

Analysis and calculator  
use)

How to: Co-Simulate  
with Cadence Spectre

Cadence tutorial : DC  
analysis and DC sweep  
in cadence

Cadence 6  
Tutorial 3 Cadence

Layout Tutorial Intro to

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~~Cadence 2: Creating a  
Simulation and~~

~~Testbench Cadence~~

~~IC6.1.6/6.1.7 Virtuoso~~

~~Tutorial -1 Part 4~~

~~(Layout Design and  
Physical Verification)~~

~~Cadence IC615~~

~~Virtuoso Tutorial 15:~~

~~Monte Carlo Analysis in  
Cadence~~

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#1 Cadence SKILL

Programming Tutorial  
for Beginners (7 lessons

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total) 2/16/2016

Simulating IV

Characteristics of

NMOS Transistor in

Cadence Virtuoso with

ADE XL Cadence

IC615 Virtuoso Tutorial

3 (HD): Using

Calculator in ADEL

Astrophotography Setup

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Creating Symbol from  
schematic in Cadence

Cadence IC615

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Virtuoso Tutorial 8:  
Stability Analysis in  
Cadence ADEL Import  
a cell library into  
cadence virtuoso How  
to make a Symbol with  
Parameters in Cadence  
Virtuoso (Black Box  
with Inputs) Intro to  
Cadence 1: Creating a  
Schematic and Symbol  
~~Cadence tutorial : How  
to plot mosfet I-V  
characteristics in~~

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~~cadence CMRR \u0026~~

~~Power dissipation~~

~~simulaiton. Phase Noise~~

~~- Ask An Engineer~~

~~Whiteboard What is~~

~~Cadence, Orcad,~~

~~Allegro, Pspice...? Other~~

~~competing software?~~

~~Cadence tutorial-~~

~~CMOS Inverter Layout~~

~~Cadence IC614/615~~

~~Virtuoso Tutorial 02~~

~~Cadence IC615~~

~~Virtuoso Tutorial 14:~~



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Using Verilog in

Cadence IC615 Design  
a CMOS inverter using

Cadence Virtuoso

~~Cadence IC615~~

~~Virtuoso Tutorial 9:~~

~~Noise Analysis in~~

~~Cadence ADEL~~

Virtuoso Tutorial Part

1: Creating a Schematic

~~Cadence IC615~~

~~Virtuoso Tutorial 12: S-~~

~~parameter analysis in~~

~~Cadence ADEL~~

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leading solution for  
accurate analog  
simulation, the Cadence  
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contains multiple solvers  
to allow a designer to  
move easily and  
seamlessly between  
circuit-, block-, and  
system-level simulation

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tasks. The foundation of the platform is a unified set of technologies shared by all of the engines—the parser, device models, Verilog-A behavioral modeling, input data formats, output data formats, etc.—thereby guaranteeing consistent and ...

Spectre Simulation

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Spectre Circuit

Simulator User Guide

Product Version 5.0 July

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Spectre Circuit

Simulator User Guide

January 2004 3 Product

Version 5.0 Preface ...

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Spectre X Simulator

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Partitioning Simulator

(XPS) provides the

FastSPICE high-

performance and

capacity simulation

needed to handle

memory and mixed-

signal designs. The

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rounded out by Spectre  
AMS Designer,  
Cadence ' s mixed-  
signal, mixed-language,  
mixed-level, functional,  
behavioral,

Spectre X Simulator -  
Cadence Design  
Systems

Cadence Circuit  
Simulator Device Model  
Equations manual. The  
Spectre circuit simulator



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is often run within the Cadence® analog circuit design environment, under the Cadence® design framework II. To see how the Spectre circuit simulator is run under the analog circuit design environment, read the Virtuoso Analog Design Environment User Guide.

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Simulator Reference

3X to 10X speed

increase and 5X

capacity improvement

while maintaining

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you to solve large-scale

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### Signal User

signal blocks and  
subsystems, while

maintaining the

accuracy expected of the

Spectre simulation

family. In addition, the

Spectre X Simulator

allows you to massively

distribute simulation

workloads, enabling

greater speed and

capacity.

Spectre X Simulator -

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Cadence Design

Systems

Virtuoso® Analog

Design Environment

User Guide Product

Version 5.1.41

September 2006

Virtuoso® Analog

Design Environment

User Guide

is to teach you to use the

Cadence Design

Environment to design

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and test digital CMOS circuits. This manual will walk you through all the necessary steps for designing and testing an inverter. First, we are going to create a schematic for the inverter. We, then, create a symbol for the inverter

Cadence Manual - Penn  
Engineering

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### Spectre Circuit

Simulator Device Model

Equations manual. The

Spectre circuit simulator

is often run within the

Cadence ® analog

circuit design

environment, under the

Cadence® design

framework II. To see

how the Spectre circuit

simulator is run under

the analog circuit design

environment, read the

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Virtuoso Spectre Circuit  
Simulator RF Analysis  
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Spectre User  
Version 6.2 June 2007

Guide

Virtuoso Spectre Circuit  
Simulator RF Analysis  
User Guide

Cadence | User Guide

Get Started 1. From the

Idle screen, press and

hold . -or- From the Idle

screen, dial and press

the SEND key . 2.

When you hear the

greeting, press the

Pound SPACE key to

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interrupt. 3. Follow the instructions to set up your new password and record a greeting.

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opened. From the CIW menus, all Cadence main tools, online help and options can be accessed. In the window area, all kind of messages (info, errors, warnings, etc) generated by the different Cadence tools appear. You can also introduce commands.

## TUTORIAL

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Cadence Virtuoso

Tutorial - USC Viterbi

User Guide -

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SpectreRF is an option

to the Spectre Circuit

Simulator from

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Systems. It adds a series

of analyses that are

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particularly useful for  
RF circuits to the basic  
capabilities of Spectre.  
SpectreRF was first  
released

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